

FIG. 1

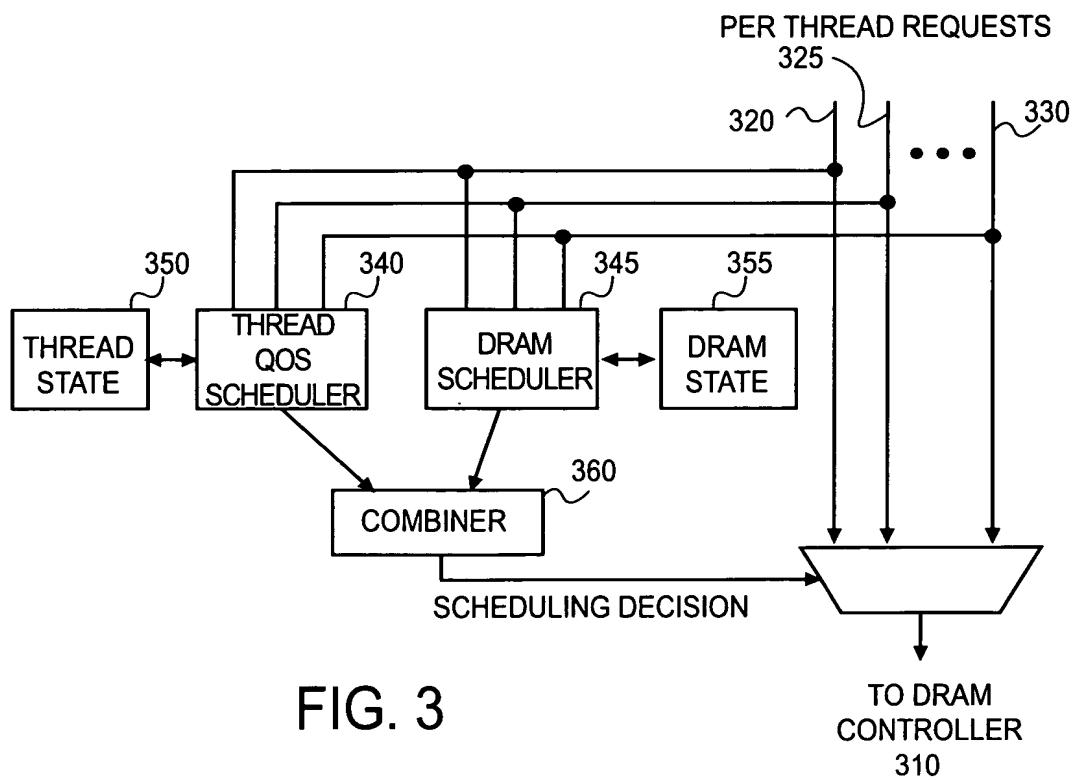


FIG. 3

TO DRAM
CONTROLLER
310

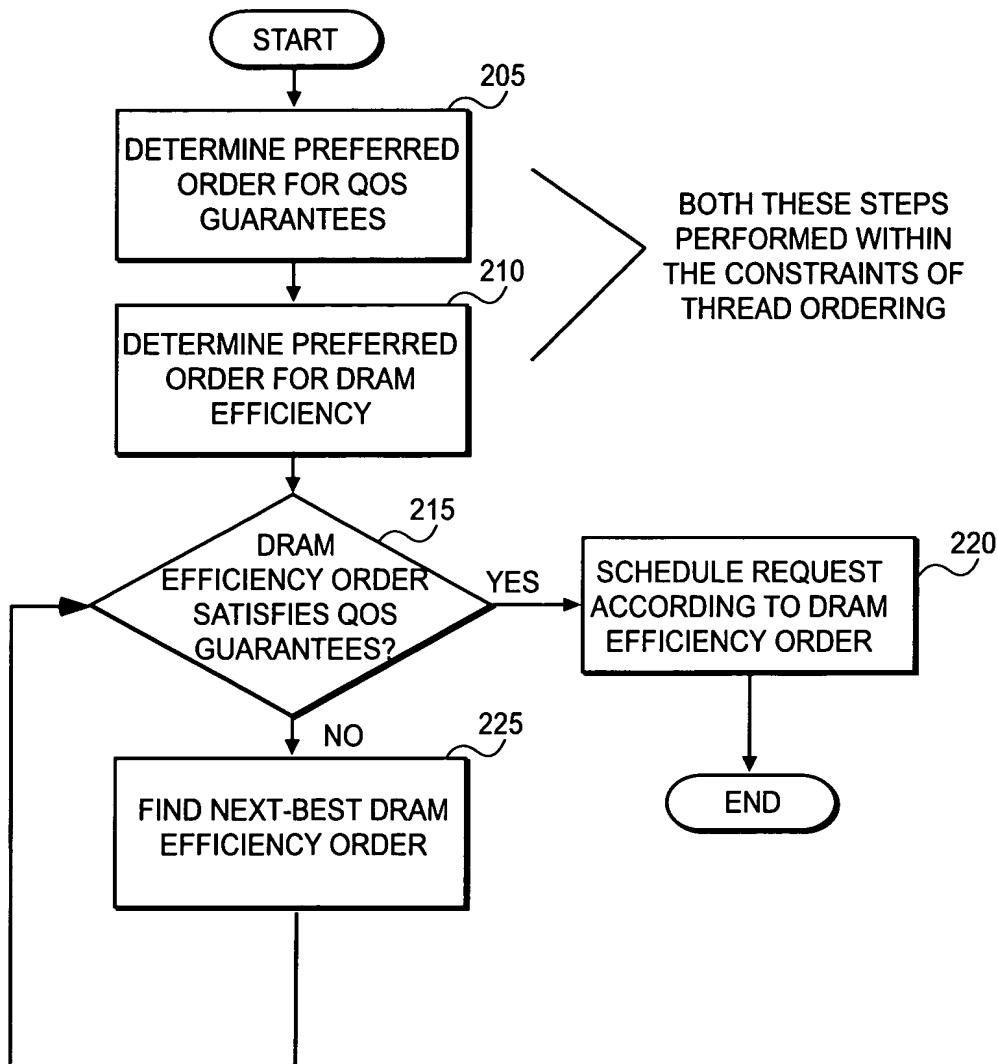


FIG. 2

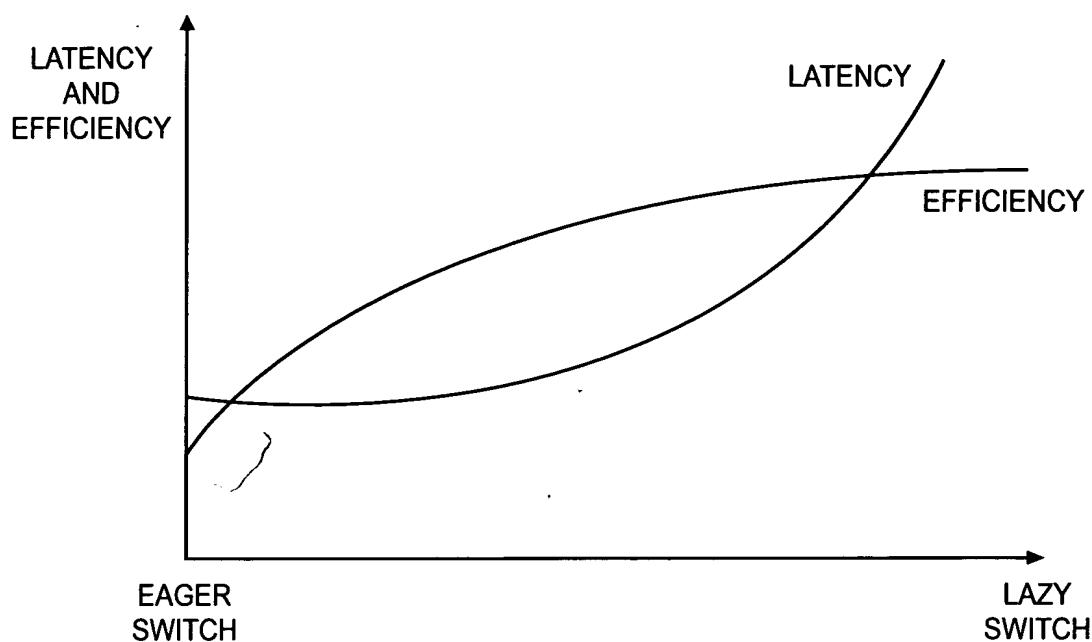


FIG. 4

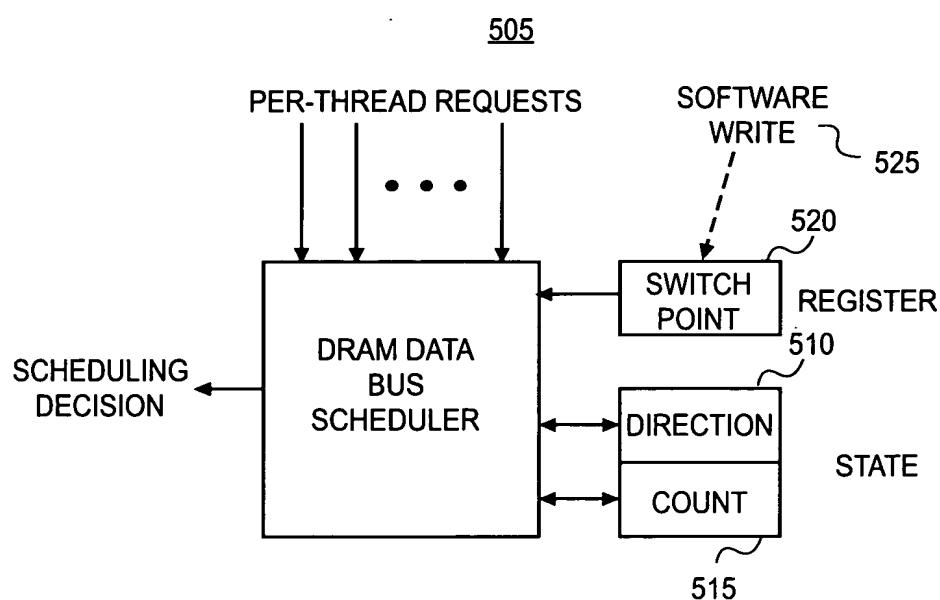


FIG. 5

2025 RELEASE UNDER E.O. 14176

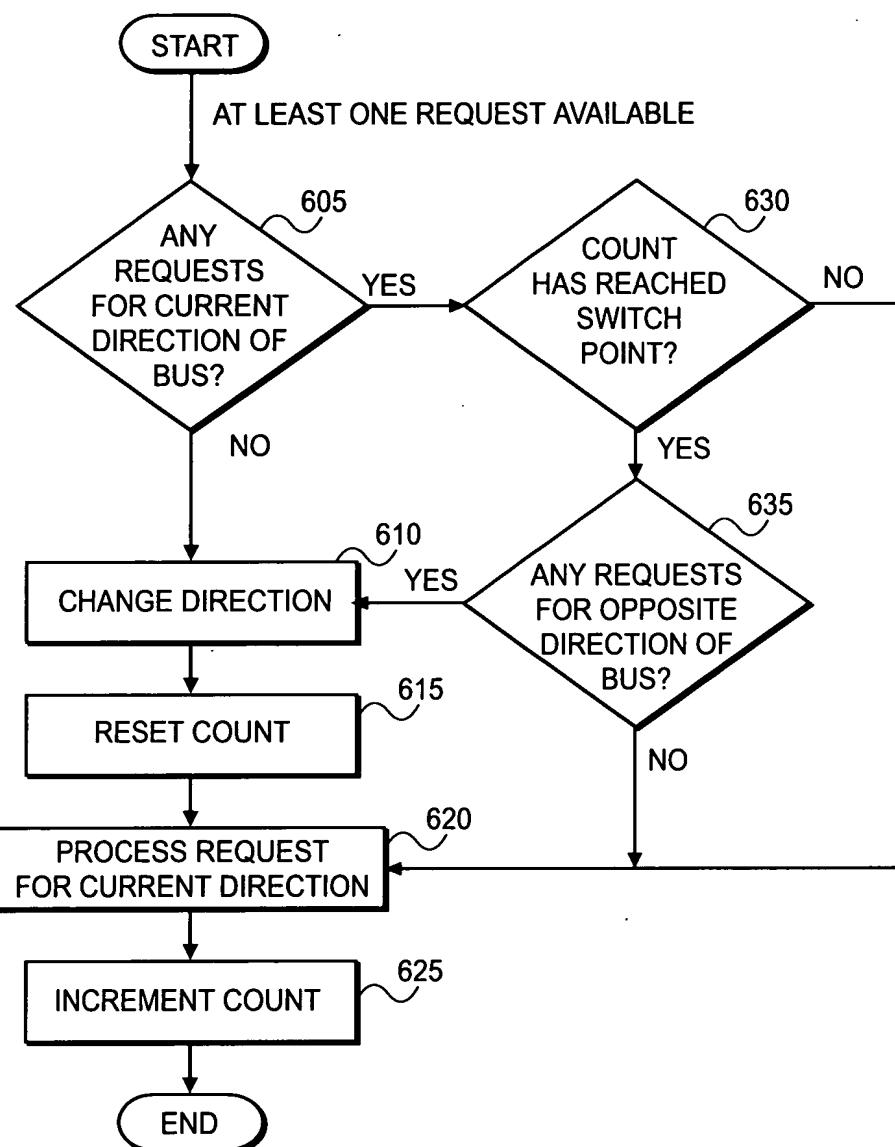


FIG. 6

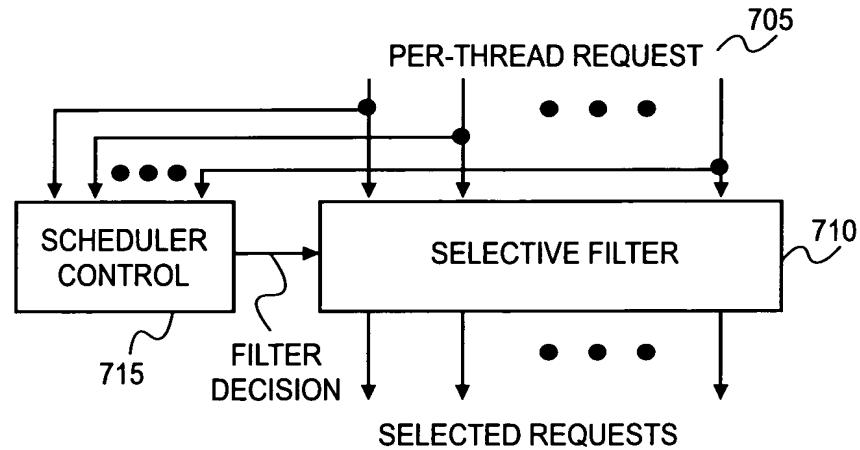


FIG. 7

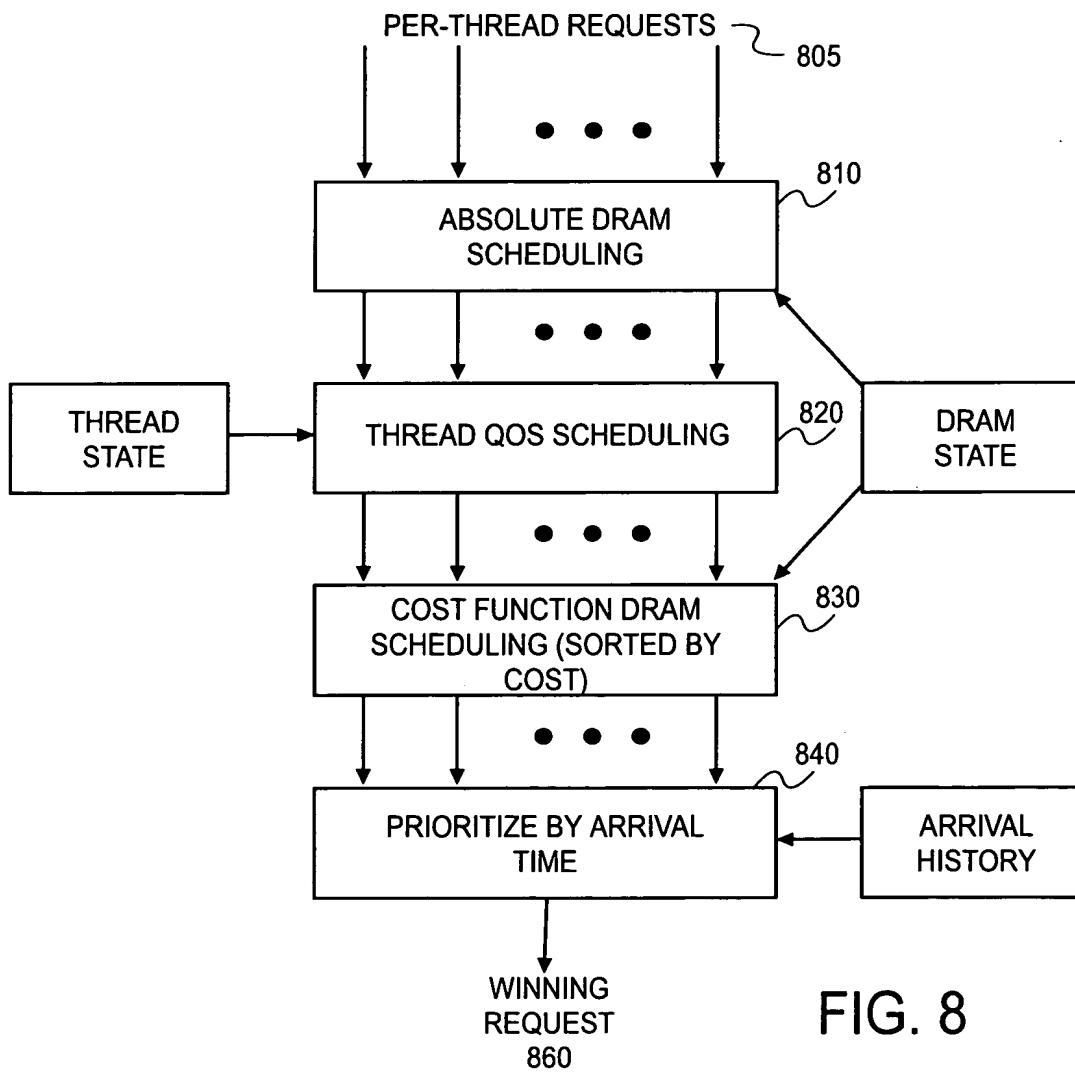


FIG. 8